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	Filing Date		2004-03-23	
	First Named Inventor	Michael J. Azevedo		
	Art Unit	2113		
	Examiner Name	Emerson C. Puenle		
Attorney Docket Number		SJO920030061US1		

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	1	S.P. LARKY, "Coverification Tames Cache and Bus Interface Controller Design", EETimes.com, pp. 1-7, [online], dated September, 1998, http://www.eetimes.com/editorial/1998/coverstory9809.html	<input type="checkbox"/>

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